

PAD1	PRE_IN1
PAD2	PRE_IN2
PAD3	PRE_IN3
PAD4	PRE_IN4
PAD5	PRE_IN5
PAD6	PRE_IN6
PAD7	PRE_IN7
PAD8	PRE_IN8
PAD9	PRE_IN9
PAD10	PRE_IN10
PAD11	PRE_IN11
PAD12	PRE_IN12
PAD13	PRE_IN13
PAD14	PRE_IN14
PAD15	PRE_IN15
PAD16	PRE_IN16
PAD17	PRE_IN17
PAD18	PRE_IN18
PAD19	PRE_IN19
PAD20	PRE_IN20
PAD21	PRE_IN21
PAD22	PRE_IN22
PAD23	PRE_IN23
PAD24	PRE_IN24
PAD25	PRE_IN25
PAD26	PRE_IN26
PAD27	PRE_IN27
PAD28	PRE_IN28
PAD29	PRE_IN29
PAD30	PRE_IN30
PAD31	PRE_IN31
PAD32	PRE_IN32

PAD79	VSS_VA
PAD80	CAL_VA
PAD81	VFS_VA
PAD82	VFS_VA
PAD83	OUTP_VA
PAD84	OUTP_VA
PAD85	IBUF_VA
PAD86	VREF_VA
PAD87	SHL_VIAS_VA
PAD88	MBIAS_VA
PAD89	PRE_BIAS_VA
PAD90	TEST_VA
PAD91	SHBIT_VA

SP	OND_VA
SP	DIVL_VA
SP	DIVS_VA
SP	DELAY_ADJ_VA
SP	DELAY_ON_VA
SP	HOLD_VA
SP	DUMMY_HOLD_VA
SP	DUMMY_RESET_VA
SP	SHFT_INL_VA
SP	SHFT_OUTL_VA
SP	SHFT_OUTR_VA
SP	TEST_ON_VA

VDD_VA	C67	2u2F
VSS_VA	C58	2u2F
DIVD_VA	C62	2u2F
VSS_VA	C63	2u2F
MBIAS_VA	C64	2u2F

PAD33	OUT1
PAD34	OUT2
PAD35	OUT3
PAD36	OUT4
PAD37	OUT5
PAD38	OUT6
PAD39	OUT7
PAD40	OUT8
PAD41	OUT9
PAD42	OUT10
PAD43	OUT11
PAD44	OUT12
PAD45	OUT13
PAD46	OUT14
PAD47	OUT15
PAD48	OUT16
PAD49	OUT17
PAD50	OUT18
PAD51	OUT19
PAD52	OUT20
PAD53	OUT21
PAD54	OUT22
PAD55	OUT23
PAD56	OUT24
PAD57	OUT25
PAD58	OUT26
PAD59	OUT27
PAD60	OUT28
PAD61	OUT29
PAD62	OUT30
PAD63	OUT31
PAD64	OUT32

